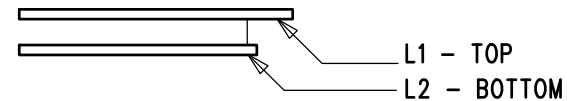


SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	NO	+/-0.003
0.035	26	X	YES	+/-0.003
0.01	54	□	YES	+/-0.003
0.06496	4	◇	YES	+/-0.003
0.12795	4	⊗	NO	+/-0.003
0.03819	16	⊗	YES	+/-0.003
0.063	9	+ ^A	YES	+/-0.003
0.02	24	+ ^B	YES	+/-0.003
0.056	16	+ ^C	YES	+/-0.003
0.07	2	+ ^D	NO	+/-0.003

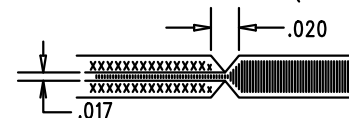
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	1ST PROTOTYPE	JON M.	12-16-11

LAYER STRUCTURE

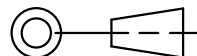


NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 2 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES.	AK
APP ENG.	JON M.
SCALE = NONE	



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TITLE: FABRICATION DRAWING

isoSPI 12-CELL BATTERY-STACK MONITOR

SIZE	IC NO.	LTC6804IG-1	REV
N/A		DEMO CIRCUIT 1894B	1

FILENAME: DC1894B-1.PCB

SHT 1 OF 1